



**St. Joseph's Institute of Technology**  
**St. Joseph's Group of Institutions**  
OMR, Chennai - 119



**Department of Electronics and Communication Engineering**  
(Accredited by NBA)

**FACULTY DETAILS**

<b>Staff Name:</b>	<b>Mrs. P. PRINCY MAGDALINE</b>
<b>Designation:</b>	Assistant Professor
<b>Date of Birth:</b>	26.02.1985
<b>Educational Qualification:</b>	M.Tech., (Ph.D)
<b>Area of Interest:</b>	Low Power VLSI
<b>Years of Experience:</b>	10 Years 9 Months
<b>Area of Research:</b>	VLSI Design
<b>No. of Students Project Guided</b>	20 Batches
<b>Publications Details:</b>	<ul style="list-style-type: none"><li>• Wireless stethoscope for recording heart sound using Li-Fi</li><li>• Realization of Efficient curve-based crypto Processor on FPGA.</li></ul>
<b>FDP &amp; Workshop Attended Details:</b>	<ul style="list-style-type: none"><li>• Evolution of Teaching Learning Process, Bharath Institute of Higher Education and Research, Chennai</li><li>• 'One day Workshop on R' organized by the Teaching Learning Centre, ICT - IIT Bombay</li><li>• Deep Learning, SRM, Chennai</li><li>• Raspberry Pi and Python, NSIC, Chennai</li><li>• Optical Network Security and Reliability Issues, Karpaga Vinayaga College of Engineering, Maduranthagam</li><li>• Robotics and Automation MNM Jain college of engineering, Chennai</li></ul>
<b>Professional Membership:</b>	NIL