



**St. Joseph's Institute of Technology**  
**St. Joseph's Group of Institutions**  
OMR, Chennai - 119



**Department of Electronics and Communication Engineering**  
(Accredited by NBA)

**FACULTY DETAILS**

<b>Staff Name:</b>	<b>Mr. M. SENTHIL MURUGAN</b>
<b>Designation:</b>	Associate Professor
<b>Date of Birth:</b>	20-07-1982
<b>Educational Qualification:</b>	B.E., M.E, (Ph.D)
<b>Area of Interest:</b>	Network Security, VLSI Design, Image Processing
<b>Years of Experience:</b>	15 Years
<b>Area of Research:</b>	Network Security
<b>No. of Students Project Guided</b>	20
<b>Publications Details:</b>	<ul style="list-style-type: none"><li>➤ Published a paper” <b>Area-Efficient and High-Speed Hardware Structure of Hybrid Cryptosystem (AES-RC4) For Maximizing Key Lifetime Using Parallel Sub pipeline Architecture</b>” in the Journal of Concurrency and Computation”: Practice and Experience (Web of Science), ISSN No 1532-0634 by April 2019.</li><li>➤ Published a paper” <b>Hardware Implementation of Hybrid Model Encryption Algorithm for Secure Transmission of Medical Images</b>” in the International Journal of Engineering and Advanced Technology(Scopus), ISSN No 2249-8958 by October 2019</li><li>➤ Published a paper” <b>Implementation of Advanced Encryption Standard Algorithm on Steganography</b>” in the International Journal of Recent Technology and Engineering (Scopus), ISSN No 2277-3878 by January 2019.</li><li>➤ Published a paper” <b>Design of Hybrid Model Cryptographic Algorithm for Wireless Sensor Network</b>” in the International Journal of Pure and Applied Mathematics (Scopus), ISSN No 1314-3395 by November</li></ul>

	2017.
<b>FDP &amp; Workshop Attended Details:</b>	<ul style="list-style-type: none"> <li>➤ Participated AICTE sponsored Faculty Development Training Programme on “<b>Research Areas in Smart Antenna Design for CUBESAT Applications</b>” at Sri Sairam Institute of Technology Chennai from 31-10-19 to 6-11-19</li> <li>➤ Participated in the short-term Training Programme on “<b>Fuzzy Sets and Applications</b>” at SRM University held on November 2018</li> <li>➤ Participated in the Training Programme on “<b>Industry 4.0</b>” by MSME – Technology Development Centre, Government of India at St. Joseph’s Institute of Technology held on November 2018</li> <li>➤ Participated in the National Level workshop on “<b>Cyber Security – Threats and Vulnerability Assessment</b>” at PSG College of Technology held on January 2016.</li> <li>➤ Participated in the National Level workshop on “<b>Real Time Signal and Image Processing Using Xilinx Vivado system Generator and Simulation of Physical layer in Communications</b>” at St. Joseph’s Institute of Technology held on September 2016.</li> <li>➤ Participated in the National Level workshop on “<b>Glimpses on Effective Research and Proposal writing</b>” at St. Joseph’s Institute of Technology held on October 2016.</li> </ul>
<b>Professional Membership:</b>	NIL